## 1<sup>st</sup> cycle EE – 3<sup>rd</sup> year – Electronics

## ANALOG ELECTRONIC CIRCUITS

Groups and times for laboratory work in year 2016/2017

(Monday,  $8^{30} - 11^{00}$ ) Group 1: from A to L

(Tuesday,  $8^{30} - 11^{00}$ ) from **M** to **Ž** Group 2:

These are groups for Slovene students. Group 2 is expected for Erasmus students.

exercise	1.	2.	3.	4. 1 <sup>st</sup> part	4. 2 <sup>nd</sup> part
1 – Mon	6. 3.	13. 3.	20. 3.	27. 3.	3. 4.
2 – Tue	7. 3.	14. 3.	21. 3.	28. 3.	4. 4.

exercise	5. 1 <sup>st</sup> part	5. 2 <sup>nd</sup> part	5. 3 <sup>rd</sup> part	6.	7.	8.
1 – Mon	10. 4.	24. 4.	25.4. Tue	15. 5.	22. 5.	29. 5.
2 – Tue	11. 4.	8. 5. Mon	9. 5.	16. 5.	23. 5.	30. 5.

Timetable can be subsequently changed. You will be informed on time.

Laboratory work will be conducted in the Laboratory of Semiconductor Electronics LPE, which is located in the basement of the newer building - at the end of the stairs in the basement, turn right, through the fire door, then turn left and at the end the second door on the right (see signs).

Manual for the laboratory work is available in the Virtual classroom e.FE. Manuals are subject to change during the semester.

Laboratory exercises are mandatory!

Erasmus students contact professor or me for list of required work.